

IN THE SPECIFICATION:

On page 10, please amend line 15 as follows:

FIG. 1 is a block diagram showing the whole arrangement of a PWM (pulse width modulation) amplifier according to a first embodiment of the present invention.

On page 15, please amend line 9 as follows:

The FET 110 controls a current flowing through a FET 112, based on the level (electric potential) of the analog signal PLLC. More specifically, when the level of the analog signal PLLC increases, the source-gate bias of the FET 110 increases to decrease the current flowing through the FET 112, whereas when the level of the analog signal PLLC decreases, the source-gate bias of the FET 110 ~~increases~~ decreases to increase the current flowing through the FET 112.

On page 19, please amend line 18 as follows:

An input signal is subjected to PWM amplification based on the triangular wave generated as above, and the resulting output signal has reduced unwanted radiation therefrom, which can cause EMI (electromagnetic interference), according to the same principle as that of the conventional PWM amplifier. The triangular wave-generating circuit 3 is thus configured to generate a triangular wave formed by pulses having different slopes, simply by adding only the D-type flip-flop 31, the switching elements 32 and 33, and the capacitor 34, to the configuration of the triangular wave-generating circuit 100 that generates a conventional triangular wave. This makes it possible to reduce manufacturing costs of the PWM amplifier in comparison with the conventional PWM amplifier.